Increasing the reliability of electrical equipment in the agricultural and industrial complex based on measuring the electrical capacitance of capacitors

Vladimir Vorzhev1, Nikolay Rudenko1,*, Pavel Malko1, and Vladimir Dergousov1

1Don State Technical University, Gagarin Sq., 1, Rostov-on-Don, 344003, Russia

Abstract. The article presents elaborated technical suggestions for increasing reliability of electrical equipment of agro-industrial complexes. The analysis of the already known ways of measuring electric capacitance is carried out and it demonstrates the necessity of withdrawing a capacitor from a working circuit for sufficient accuracy of determining electric capacitance. The urgency of developing a method for measuring electric capacitance without removing the capacitor from a working circuit to reduce the average recovery time is shown. Theoretical foundation of the possibility to measure electric capacitance without a capacitor being withdrawn from a working circuit in smoothing filter mode is worked out; a structural electric circuit of an electric capacitance measuring device is elaborated and the principle of its operation is described; methods of measurement by means of the given device are elaborated; conditions for creating and maintaining the measurement mode of the device are substantiated. The validity of the received results is checked with the help of the programmed package Mathcad, as well as on electric models of the main blocks of the device. The obtained results can be used to measure electric capacitance of electrolytic capacitors during maintenance and electrical equipment renovation and also can promote its reliability.

1 Introduction

Efficiency of an agro-industrial complex (AIC) can be improved by automation and electrification of all technological processes. In particular, power installations and various electro-technical equipment containing capacitors are widely used in AIC. They include starting controllers of electric drives, heating and lighting systems, smoothing filters of power converters and power units, reactive power compensation devices, power factor correction devices and many others [1, 2].

However, capacity of electrical capacitors decreases due to their natural wear, which leads to poorer performance of power installations.

Thus, electrolytic capacitors eventually deteriorate because electrolytes dry out. This mainly concerns aluminum capacitors, however, small-capacity tantalum capacitors are...
prone to wear as well [3, 4]. Nonetheless, they appear to have no visible flaws, which makes diagnostics significantly more complicated. Therefore, to control electrical capacity of condensers is a critical maintenance and renovation operation [5, 6].

One of the possible ways to increase the reliability of electrical equipment of AIC is improving availability rate at the expense of reduced average recovery time [7-10]. However, the analysis of the already known methods of measuring electrical capacitance shows that to reach sufficient measurement accuracy requires withdrawal of a capacitor from a measuring RC-circuit [11-16]. This leads to an increase in the average recovery time. Therefore, to resolve the existing contradiction, it is urgent to develop a method for measuring electrical capacitance, which makes it possible to reduce the average recovery time.

2 Research material

The objective of the given article is elaboration of technical suggestions for improving reliability of electric equipment of AIC on the basis of measuring electrical capacitance without withdrawing a capacitor from a working circuit. To attain this objective, it seems reasonable to complete the following particular tasks:

• To provide theoretical foundation of the possibility to measure electrical capacitance of a capacitor without withdrawing it from a working circuit in smoothing filter mode,
• To elaborate a structural circuit of an electrical capacity measurement device and describe the principle of its work,
• To work out a method of measuring electrical capacitance without withdrawal of a capacitor from a working circuit in smoothing filter mode,
• To provide justification of serviceability conditions and accuracy of measurement which is sufficient for solving engineering tasks by means of the given device.

2.1 Theoretical foundation

Theoretical foundation of the possibility to measure electrical capacitance without withdrawing a capacitor from a working circuit in smoothing filter mode. It is known that while a capacitor is working on quite a high frequency (for which the ratio $\frac{X_C}{R}$ is met), voltage on its plates $u_C(t)$ represents a certain superposition of the dc component $U_0$ and the ac component, the amplitude of which is, as a rule, much lower than the quantity $U_0$, which is $U_{\text{m, pulse}} \ll U_0$. This electric condition of a capacitor is in keeping with smoothing filter mode, the circuit of which is shown on Fig. 1.

![Fig. 1. A typical circuit of engaging a capacitor into a smoothing filter circuit.](image)

Apart from that, if transient stationary processes are established and under certain conditions the shape of the ac component is known to be triangle (i.e. linear), as is shown on Fig. 2. This signal waveform evidently means that upon charge and discharge exponential time dependences can be to a first approximation replaced with linear ones with insignificant sacrifice of ac-uracy of description of the given process.
By means of the theory of describing transient processes in linear electrical circuits we obtain an analytical expression for the value of the ripple factor of the smoothing filter shown on Figure 1 under the condition of preserved linear shape of the ac voltage component \( u_C(t) \).

It is known that the process of charge of such a circuit being under action of rectangular pulses is described via the following expression (1):

\[
 u_{C\text{char}} = \frac{R \cdot U_m}{r + R} \left(1 - e^{-\frac{t}{\tau_{\text{char}}}}\right); \quad \tau_{\text{char}} = \frac{r \cdot R \cdot C}{r + R},
\]

where \( \tau_{\text{char}} \) – relaxation time of a \( RC \)-circuit in the charging procedure.

Discharge procedure of this circuit is described via expression (2):

\[
 u_{C\text{dis}} = \frac{R \cdot U_m}{r + R} e^{-\frac{t}{\tau_{\text{dis}}}}; \quad \tau_{\text{dis}} = R \cdot C,
\]

where \( \tau_{\text{dis}} \) – relaxation time of a \( RC \)-circuit in the discharging procedure.

Expression (1) is now modified on the assumption of a stationary transient process going on where periodic voltage modulation \( u_C(t) \) occurs within voltage intervals from \( U_1 \) to \( U_2 \) (Fig. 2).

Full solution of a charging process differential equation looks as follows:

\[
 u_{C\text{char}}(t) = A e^{-\frac{t}{\tau_{\text{char}}}} + \frac{R \cdot U_m}{r + R}.
\]

In order to determine the unknown constant \( A \) we consider voltage to be equal to \( U_1 \) at \( t = +0 \) according to commutation laws. Therefore, according to (3):

\[
 A = U_1 - \frac{R \cdot U_m}{r + R}.
\]

By completing (3) with (4), we obtain:
\[
u_{\text{char}}(t) = \left(U_1 - \frac{R \cdot U_m}{r + R}\right)e^{-\frac{t}{\tau_{\text{char}}}} + \frac{R \cdot U_m}{r + R}.
\] (5)

Then, on impulse time expiry \(t_{\text{pul}}\), the capacitor will charge up to voltage \(U_2\), which is:

\[
U_2 = \left(U_1 - \frac{R \cdot U_m}{r + R}\right)e^{-\frac{t_{\text{pul}}}{\tau_{\text{char}}}} + \frac{R \cdot U_m}{r + R}.
\] (6)

Next, as impulse expires and a pause with duration \(t_p\) starts, voltage on the capacitor will drop to \(U_1\) again, which is:

\[
U_1 = U_2 e^{-\frac{t_p}{\tau_{\text{dis}}}}.
\] (7)

Expressions (6) and (7) represent the equation system regarding the unknown \(U_1\) and \(U_2\), the solution of which is shown below.

\[
U_1 = \frac{R \cdot U_m}{r + R} \cdot \frac{e^{-\frac{t_p}{\tau_{\text{dis}}}} - e^{-\frac{t_{\text{pul}}}{\tau_{\text{char}}}} \cdot e^{-\frac{t_p}{\tau_{\text{dis}}}}}{1 - e^{-\frac{t_{\text{pul}}}{\tau_{\text{char}}} \cdot e^{-\frac{t_p}{\tau_{\text{dis}}}}}};
\] (8)

\[
U_2 = \frac{R \cdot U_m}{r + R} \cdot \frac{1 - e^{-\frac{t_{\text{pul}}}{\tau_{\text{char}}} \cdot e^{-\frac{t_p}{\tau_{\text{dis}}}}}}{1 - e^{-\frac{t_{\text{pul}}}{\tau_{\text{char}}} \cdot e^{-\frac{t_p}{\tau_{\text{dis}}}}}}.
\] (9)

We suggest that due to the conditions of transient processes the voltage shape on the capacitor be considered linear. Then exponential character of time dependences can be represented as linear by means of a Taylor series expansion up to first-order summands:

\[
e^{-\alpha x} \approx 1 - \alpha x, \quad x \to 0.
\] (10)

Then, the duty cycle of the rectangular pulses source is supposed to equal to 2, i.e. \(t_{\text{pul}} = t_p = T/2\).

In this case expressions (8) and (9) become:

\[
U_1 = \frac{R \cdot U_m}{r + R} \cdot \frac{\tau_{\text{dis}}}{\tau_{\text{char}} + \tau_{\text{dis}} - T/2} \cdot \left(1 - \frac{T/2}{\tau_{\text{dis}}} \right);
\] (11)

\[
U_2 = \frac{R \cdot U_m}{r + R} \cdot \frac{\tau_{\text{dis}}}{\tau_{\text{char}} + \tau_{\text{dis}} - T/2}.
\] (12)

By means of expressions (11) and (12), and allowing for the linear shape of the signal on capacitor clamps the constant component value of signal \(U_0\) and pulse amplitude \(U_{m\text{pulse}}\) is determined:

\[
U_0 = \frac{U_1 + U_2}{2} = \frac{R \cdot U_m}{r + R} \cdot \frac{\tau_{\text{dis}} - T/4}{\tau_{\text{char}} + \tau_{\text{dis}} - T/2};
\] (13)
Then the pulse coefficient of this signal is:

\[ K_p = \frac{U_{\text{mpul}}}{U_0} = \frac{T/4}{\tau_{\text{dis}} - T/4}. \]  (15)

In the strict sense, expression (15) is not a pulse coefficient because it is the fundamental harmonic amplitude \( U_{\text{m \_main}} \) that must be in the numerator of this expression. However, it is not critical from the point of view of determining the electrical capacitance value, since practical value of expression (15) lies in its being determined only by the discharge time of the circuit under study \( \tau_{\text{dis}} = RC \), which makes it possible (with known values of resistance \( R \) and pulse repetition frequency \( f_{\text{pul}} \)) to determine the electrical capacitance value \( C \) with sufficient accuracy for engineering calculations.

As it can be seen from (15) it is more convenient to use value \( 1/K_p \) for programming a microcontroller, as its use leads to a simpler expression for value determination:

\[ \frac{1}{K_p} = \frac{4\tau_{\text{dis}}}{T} - 1. \]  (16)

In this case, the required value of electric capacitance \( C \) will be determined via the following formula:

\[ C = \frac{T}{4R} \left( \frac{1}{K_p} + 1 \right) = \frac{1}{4R \cdot f_{\text{pul}}} \left( \frac{1}{K_p} + 1 \right), \]  (17)

where \( f_{\text{pul}} \) is rectangular pulse repetition frequency of a duty cycle equal to 2.

Therefore, given the values of resistance \( R \) and pulse repetition frequency \( f_{\text{pul}} \), and also the known (measured) values of the signal \( U_0 \) dc component and pulse amplitude \( U_{\text{m \_pulse}} \), electric capacitance can be calculated according to formulas (15) and (17).

### 2.2 Elaboration of a structural electric circuit of the electric capacitance measuring device and description of its operation principle

As it was mentioned above, the work principle of the device is to measure the following quantities: the dc component \( U_0 \) and voltage pulse amplitude \( U_{\text{m \_pulse}} \) on capacitor clamps with the known value of \( k \)th rectangular pulse repetition frequency \( f_{\text{pul \_k}} \). At the same time the necessary signal form is achieved by monitoring quantity \( U_{\text{m \_pulse}} \), the value of which must always be lower than a certain limit quantity \( U_{\text{m \_max}} \), determined by means of numerical calculations for chosen measuring sub-bands.

The electric structural circuit of the device is shown on Fig. 3. Forming of rectangular pulse repetition with fixed frequency \( f_{\text{pul \_k}} \) (there are 4 clock frequencies in the given circuit) is carried out by means of a clock frequency generator (CFG), the output of which is connected to a clock input of a frequency divider (FD), which initiates count pulses on its outputs \( Q_1, Q_2, Q_3 \) corresponding to frequencies \( f_{\text{pul}}/2, f_{\text{pul}}/4, f_{\text{pul}}/8 \), respectively. These signals arrive to multiplex inputs (M) \( I_1, I_2, I_3 \) (a signal with frequency \( f_{\text{pul}} \) arrives to input \( I_0 \) directly from the CFG output), and pulses from certain inputs with a number
corresponding to a control word preset by driving points $A_0, A_1$ appear on the multiplex output (Y).

Fig. 3. A structural circuit of a measuring device of electric capacitance of a capacitor.

The choice of frequency $f_{j\text{ puls}}$ is carried out by means of a univibrator (Un), which generates single rectangular pulses of fixed width coming from its output to the input of a binary counter (BC) and making the figure on its output increase by 1. Counting in a BC can only reach a number corresponding to the number of possible frequencies, then it clears the registers.

Rectangular pulse sequence of the chosen frequency $f_{j\text{ puls}}$ comes to a pulse forming unit (PSU) consisting of a two-stage transistor switch ($V_{T1}, V_{T2}$) and a measuring chain of a RC-circuit ($R_3, R_4, C$), where $C$ is a capacitor of required capacitance.

Then, through voltage followers $VF_1, VF_2$ (assembled on operational amplifier microcircuits can serve this function as well) the measuring signal comes to the forming units of the dc and ac components.

Forming of the dc component occurs by means of a low-pass filter intended for a minimal pulse repetition frequency $f_{j\text{ puls min}}$. Forming of the ac component occurs with the help of a high-pass filter, also intended for a minimal pulse repetition frequency $f_{j\text{ puls min}}$.

Monitoring the value of pulse amplitude is carried out via a comparator (C) which at any one time compares half-period average voltage (and amplified up to the level where voltage drop on a rectifier diode does not make a significant impact on its value) of the ac component $U_{\text{aver}}$ with certain standard voltage $U_{\text{sr}}$, removed from the output of the reference voltage source (RVS). While the condition $U_{\text{aver}} < U_{\text{sr}}$ is fulfilled, voltage on the comparator output is at a low level and an alarm unit (AU) connected to its output does not give a light or (and) sound signal. But if the condition $U_{\text{aver}} > U_{\text{sr}}$ is fulfilled, a high-level signal appears on the comparator output, which supplies the alarm unit with electricity. It shows to the operator that this frequency is inapplicable for a RC-circuit in measuring mode.

After that, by short-term pressing of key S1 the operator gives a single pulse to the input of the binary counter (BC), therefore increasing the control word on the multiplex driving points ($A_0, A_1$) by 1. Then the whole process repeats until the condition $U_{\text{aver}} < U_{\text{sr}}$ is fulfilled and the AU does not give a light and (or) a sound signal. It shows to the operator...
that the chosen frequency corresponds to the measured capacitance and that the device turns to measurement mode.

In electric capacitance measuring mode the input of a calculating device (CD) (not shown in the figure) receives a digital signal corresponding to the chosen frequency $f_{\text{pul}}$, as well as analog signals corresponding to the dc component of signal $U_0$ and pulse amplitude $U_{m\text{ pul}}$. A CD can be assembled on the basis of a microcontroller with analog-digital transformers and a memory device which contains an electric capacitance calculation programmed according to formulas (15), (17) and the value of resistance $R4$. The calculation result can be brought onto the input of a digital indication for the operator to take the indications.

2.3 Elaboration of electric capacitance measuring method without withdrawing a capacitor from a working circuit in smoothing filter mode

The given method contains the following sequence of actions:

- To cut off the electric equipment (EE) (Fig. 3), containing a capacitor with required capacitance from an external electric energy source.
- To connect the device to clamps of a capacitor of required capacitance with the help of probes and remove residual charge by means of switching the SA switch to position 2-2'. At the same time the operator controls discharge current drop of the capacitor to zero with the help of galvanometer P1.
- To switch the SA switch to position 1-1' and choose pulse repetition frequency with the help of the alarm unit AU and they key S1 as described in 2.2.
- To measure the chosen frequency $f_{\text{pul}}$, the dc component of signal $U_0$, pulse amplitude $U_{m\text{ pul}}$, to calculate and take the readings of electric capacitance value.

2.4 Justification of conditions of efficiency and sufficient accuracy of the given device

Concerning the conditions that provide voltage of linear shape on capacitor clamps, according to (10), they can be expressed by the following ratios:

$$\frac{T}{2} < \tau_{\text{char}}, \quad \frac{T}{2} < \tau_{\text{dis}}.$$  \hspace{1cm} (18)

However, this condition cannot be fulfilled on one fixed pulse repetition frequency $f_{\text{pul}}$ at determining values of capacitances in a sufficiently broad band. One of the ways to fulfill this condition is to limit pulse amplitude of output voltage with a possibility to select pulse repetition frequency from a prefixed set of $n$ frequencies $f_{\text{pul}1}, f_{\text{pul}2}, \ldots, f_{\text{pul} n}$.

Due to complexity of mathematical transformations the research data were carried out via the application programmed Mathcad, which enabled determining maximum permissible value of pulse amplitude $U_{m\text{ max}}$. In its turn, it provides linear shape of the output signal for the fixed band of measuring capacitances $C_{\text{min}}...C_{\text{max}}$. For the given device it is 0.15 V.

Table 1 provides data for measuring sub-bands of capacitances and corresponding clock frequencies obtained by means of calculations in Mathcad.
### Table 1. Measuring capacitance sub-bands and corresponding clock frequencies.

<table>
<thead>
<tr>
<th>Measuring sub-bands, μF</th>
<th>Clock frequency, Hz</th>
</tr>
</thead>
<tbody>
<tr>
<td>2000 - 5100</td>
<td>50</td>
</tr>
<tr>
<td>1000 - 2000</td>
<td>100</td>
</tr>
<tr>
<td>500 - 1000</td>
<td>200</td>
</tr>
<tr>
<td>250 - 500</td>
<td>400</td>
</tr>
</tbody>
</table>

### 3 Conclusions

- The possibility of measuring electric capacity without withdrawing a capacitor from a working circuit in smoothing filter mode is proved. Also, calculation ratios are obtained which enable measuring electric capacitance of an electrolytic capacitor by means of altering the dc component and the amplitude of the ac component on its plates, taking into account the linear shape of the ac voltage component.
- A structural circuit of an electric capacitance measuring device without withdrawing a capacitor from a working circuit in the smoothing filter mode.
- A method of measuring electric capacitance without withdrawing a capacitor from a working circuit in smoothing filter mode is elaborated.
- The conditions of efficiency and accuracy sufficient for engineering calculations of the given device, as well as measuring sub-bands of capacitance and their corresponding clock frequencies are justified.
- Introducing of the given technical suggestions on measuring electric capacitance of capacitors without withdrawing a capacitor from a working circuit will promote reliability of electric equipment of agro-industrial complexes.

### References

1. E.A. Kireeva, *A full guide on electric equipment and electrical engineering (with calculation examples)* (KNORUS, M., 2012)